

NVIDIA RDSS Intern Program

2021 VLSI Physical Design | Tegra SW | GPU, CUDA ◦ 研發替代役 ◦



VLSI Physical Design

- Work on floorplan, power/clock distribution, placement, routing, timing/power/noise analysis, chip assembly, and physical verification
- MS/PhD in Electrical Engineering or related major courses taken in IC design or digital circuit design
- Hands-on in EDA tools, Familiar with Perl/Tcl/Shell/Python

Tegra Software Engineer

- Develop firmware and software for NVIDIA Security and System Software
- MS/PhD in Computer Science/EE
- Excellent C skills/ Linux or Android
- Work Independently and highly motivated.

Developer Tech. SW Engineer – AI

- Working with key application developers to create and optimize core parallel algorithms and data structures to provide the best solutions using GPUs.
- MS/PhD in CS/EE
- Experience with parallel programming, ideally CUDA, OpenCL, OpenACC.

Please send your English resume, Transcript to yvochen@nvidia.com

NVIDIA Culture <https://www.youtube.com/watch?v=DVvEzFHTy1A&feature=youtu.be>

